Om Jee Pandey

Assistant Professor



Department of Electronics Engineering Indian Institute of Technology (BHU), Varanasi Varanasi, 221005, India

Sep. 03, 2025

NOTICE

With reference to the **Dean (Academic Affairs), IIT (BHU) Varanasi, office letter** [Ref. No.: IIT(BHU)/TEC/Ph.D.Evl./EC/1448/646, Dated: September 01, 2025], the **Ph.D. Oral Examination** of **Ms. Neha Sharma** (Roll No.: 22091004), a research scholar of the Department of Electronics Engineering, is scheduled as follows:

• Date: Thursday, September 11, 2025

• Time: 9:30 AM

• Venue: Committee Room, Department of Electronics Engineering

• Mode: Hybrid (https://meet.google.com/duq-hmzv-sqg)

• Thesis Title: "Intelligent Node Fault Prediction and Route Optimization Mechanisms for IoT Networks"

All members of the **Oral Examination Board** are kindly requested to make it convenient to attend and conduct the examination. Interested Faculty members are also cordially invited.



Dr. Om Jee Pandey

Supervisor & Chairman Oral Board

CC:

- 1. Prof. Phaneendra K. Yalavarthy, IISc Bangalore
- 2. Prof. Rajesh Kumar Pandey, Mathematical Science, IIT (BHU) Varanasi
- 3. Dr. Prasenjit Chanak, Department of Computer Science & Engineering, IIT (BHU) Varanasi
- 4. Dr. Priya Ranjan Muduli, Department of Electronics Engineering, IIT (BHU) Varanasi
- 5. Head, Department of Electronics Engineering, IIT (BHU) Varanasi
- 6. Convener DPGC, Department of Electronics Engineering, IIT (BHU) Varanasi
- 7. All the faculty members of the Department of Electronics Engineering, IIT (BHU) Varanasi
- 8. Dean (Academic Affairs), IIT (BHU), Varanasi
- 9. Dr. Atul Kumar, Electronics Engineering, IIT (BHU) Varanasi, for e-broadcast

Om Jee Pandey Supervisor & Chairman Oral Board

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